

**Claims**

1. A circuit arrangement for startup current  
5            limitation for electronic moduls connected to a  
             module carrier with a multitude of slots,  
             characterized in that each electronic module  
             (20) is associated for time-delayed power  
             supply that varies from slot to slot with a  
10            comparator connected to the respective slot (4  
             to 19) to which a reference voltage is applied  
             and upstream of which charging capacitors (35  
             to 38) are provided which have different  
15            capacitances and are connected in different  
             numbers and capacitances to the respective slot  
             (4 to 19), the varying capacitance totals  
             determining the length of the startup delay  
             where exceeding the reference voltage after the  
20            respective charging time represents a signal  
             for applying the operating voltage to the  
             respective electronic module (20).
2. The circuit arrangement according to claim 1,  
25            characterized in that resistors (31, 32) are  
             connected upstream of the comparator (28) for  
             providing the reference voltage and charging  
             resistors (33, 34) are connected upstream of  
             the charging capacitors (35 to 38).
- 30            3. The circuit arrangement according to claim 1,  
             characterized in that the slots (4 to 19) of  
             the module carrier (1) comprise power terminals  
             (4a,b to 19a,b) for the electronic module (20)  
             and terminals (4c-f to 19c-f) for the charging  
35            capacitors (35 to 38), the terminals (4c-f to  
             19c-f) being connected in varying

configurations to the respective terminal (4b to 19b).

- 5           4. The circuit arrangement according to claim 1,  
characterized in that the electronic module  
(20) is an evaluating unit for recording and  
analysing measuring signals that is connected  
to sensors (21).

10